

Lesson Plan for Even semester

Govt. Polytechnic, Jhajjar

Name of the Faculty: Shakti Raj Singh
Discipline: Computer Engineering
Semester: 4th
Subject: Microprocessors and Peripheral Devices
Lesson Plan Duration: 15 weeks (from March 2022 to June 2022)

Work Load (Lecture/ Practical) per week (in hours): Lect- 03 Pract-06

Week	Theory		Practical	
	Lecture day	Topic (including assignment/ test)	Practical day	Topic
1 st	1 st	Typical organization of a microcomputer system.	1 st	A brief history of Microprocessor. How Microprocessor works and its various features.
	2 nd	Functions of its various blocks.		
	3 rd th	Microprocessor and its evolution.		
	4 th	Function and impact of microprocessor on modern society.		
2 nd	5 th	Concept of Bus.	2 nd	Familiarization of different keys of 8085 microprocessor kit.
	6 th	Bus organization of 8085.		
	7 th	Functional block diagram of 8085 and function of each block.		
	8 th	Pin details of 8085 and related signals.		
3 rd	9 th	Demultiplexing of address/data bus generation of read/write control signals.	3 rd	Familiarization of memory map of 8085.

	10 th	Steps to execute a stored Programme.		
	11 th	Instruction cycle, machine cycle and T-states.		
	12 th	Fetch and execute cycle.		
4 th	13 th	Revision and assignments	4 th	Steps to enter data/program on 8085 kit.
	14 th	TEST		
	15 th	Brief idea of machine and assembly languages, Machines and Mnemonic codes, Instruction format and Addressing mode.		
	16 th	Identification of instructions as to which addressing mode they belong.		
5 th	17 th	Concept of Instruction set and Explanation of the instructions of the following groups of instruction set.	5 th	Steps to modify data/program on 8085 kit
	18 th	Data transfer group		
	19 th	Revision and assignments		
	20 th	TEST		
6 th	21 st	Arithmetic Group, Logic Group	6 th	Steps to execute a programme on 8085 kit.
	22 nd	Stack, I/O and Machine Control Group		
	23 th	Revision and assignments		
	24 th	TEST		

7 th	25 th	Programming exercises in assembly language	7 th	Writing and execution of ALP for addition of two 8 bit numbers.
	26 th	Concept of memory mapping		
	27 th	Partitioning of total memory space		
	28 th	Revision and assignments		
	29 th	Revision and assignments		
8 th	30 th	TEST	8 th	Writing and execution of ALP for subtraction of two 8 bit numbers.
	31 st	Address decoding		
	32 nd	Concept of peripheral mapped I/O		
	33 rd	Concept of memory mapped I/O		
9 th	34 th	Interfacing of memory mapped I/O devices	9 th	Writing and execution of ALP for multiplication and division of two 8 bit numbers.
	35 th	Revision and assignments		
	36 th	TEST		
	37 th	Concept of interrupt		
10 th	38 th	Maskable and non-maskable	10 th	Writing and execution of ALP for arranging 10 numbers in ascending/descending order.
	39 th	Edge triggered and level triggered interrupts		
	40 th	Software interrupt		
	41 st	Restart interrupts and its use		
11 th	42 nd	Various hardware interrupts of 8085	11 th	Writing and execution of ALP for 0 to 9 BCD counters (up/down counter according to choice stored in memory).
	43 rd	Servicing interrupts		
	44 th	Revision and assignments		
	45 th	extending interrupt		

12 th		system	12 th	Interfacing exercise on 8255 like LED display control and 8253 programmable interval timer.
	46 th	Concept of programmed I/O operations		
	47 th	synchronous data transfer, asynchronous data		
		transfer (hand shaking)		
	48 th	Interrupt driven data transfer		
13 th	49 th	DMA	13 th	Interfacing exercise on 8279 programmable KB/display interface like to display the hex code of key pressed on display.
	50 th	Serial output data, Serial input data		
	51 st	8255 PPI		
	52 nd	8253 PIT		
14 th	53 rd	8257 / 8237 DMA controller,	14 th	Use of 8085 emulator for hardware testing.
	54 th	Programmable KB/Display Interface,		
	55 th	8251		
		Communication Interface Adapter.		
	56 th	Revision and assignments		
15 th	57 th	Revision and assignments	15 th	Revision and Problem Solving.
	58 th	Revision and assignments		
	59 th	Revision and assignments		
	60 th	Revision and assignments		

LESSON PLAN

Name of faculty :Vivek dahiya, Lecturer (G.P. Jhajjar)

Discipline :Computer Engineering

Subject : OOPJ (4thsem)

Lesson plan duration :15 weeks(March 2022 to June

2022) Workload(lecture/practical) per week:Lectures-

03,practicals-06

week	Theory		Practical	
	Lecture Day	Topic(including assignment/test)	Practical	Topic
1 st	1 st	Introduction and Features :Fundamentals of Object oriented programming – procedure oriented programming Vs. object Oriented programming (OOP.) Object oriented programming concepts– Classes, object, object reference	1 st	Program of basic OOP in java.
	2 nd	<i>Abstraction ,encapsulation Inheritance,</i>		
	3 rd	<i>Inheritance, polymorphism, Introduction of eclipse(IDE)for developing programs in Java</i>	2 nd	Program of basic OOP in java.
2 nd	1 ST	Language Constructs :Review of constructs of C used in JAVA :	1 st	ConsiderwehaveaClassofCars underwhichSantroXing,AltoandWagonRepresentsindividualObjects.InthiscontexteachCar Objectwillhaveitsown,Model,Year ofManufact.,Color,TopSpeed, etc.whichformPropertiesofthe Carclassand the associated actions i.e., object Functions like Create(),Sold(),display()formtheMethodsofCarClass.Use this classtocreateanotherclassCompanythattracksthemodelist create.
	2 ND	data types, increment and decrement operators		
	3 RD	Relational and logical operators, if else then clause		
			2 nd	SoftwareEngineers,ModuleLead,TechnicalLead,ProjectLead,ProjectManager,

				Program Manager, Directors all are the employees of the company but their work, perks, roles, responsibilities differs. Create the Employee base class would provide the common behavior so of all types of employee and also some behaviors properties that all employee must have for that company. Also include search method
3 rd	1 st	Conditional expressions ,input using scanner class	1 st	Suppose the Airport person all want to maintain records of the arrival and departure of the planes. Create a class Airport that has data like name, id, and address.
	2 nd	Input using scanner class and output statement,		
	3 rd	Output statement Loops,	2 nd	Practice of practical's.
4 th	1 st	Switch case	1 st	. Create a whole menu driven hospital management system using concept of OOP like classes, inheritance. Include information about the following: a. Patient - name, registration id, age, disease, etc. b. Staff - id, name, designation, salary, etc. c.
	2 nd	Arrays		
	3 rd	Methods	2 nd	Practice of practical.
5 TH	1 st	Classes and Objects: Creation, accessing class members	1 st	Create a class called Musician should contain three methods string(), wind() and perc(). Each of these methods should initialize a string array to contain the following instruments: veena, guitar, sitar, sarod and mandolin under string() - flute, clarinet, saxophone, nadaswaram and piccolo under wind()- tabla, mridangam, bangos, drums and tambour under perc
	2 nd	Private Vs Public Vs Protected Vs Default		
	3 rd	Revision /Test	2 nd	Practice of practical.
6 TH	1 st	Constructors	1 st	Write three derived classes inheriting functionality of

	2 nd	Object & Object Reference		baseclassperson(shouldhaveamemberfunctionthatastasktoenternameandage)andwithadddeduniquefeaturesofstudent,andemployee,andfunctionalitytoassign,changeanddeleterecordsofstudentandemployee.
	3 rd	Object & Object Reference	2 nd	Practice of practical's.
7 th	1 st	Inheritance: Definition of inheritance, protected data,	1 st	Usingtheconceptofmultipleinheritancecreateclasses:Shape,Circle,Square,Cube,Sphere,Cylinder. Yourclassesmayonlyhavetheclassvariables specifiedinthetablebelowandthemethodsAreaand/orVolumetooutputtheirareaand/orvolume.
	2 nd	Private data, public data,		
	3 rd	Constructor chaining, order of invocation	2 nd	Write a program to create Class Person.
8 th	1 st	Order of invocation, types of inheritance,	1 st	To create class STUDENT inherit from Person
	2 nd	Single inheritance Multilevel inheritance,	2 nd	To create class Instructor inherits from Person.
	3 rd	Hierarchical inheritance		
9 th	1 st	Hierarchical inheritance Hybrid inheritance	1 st	To create class Instructor inherit from Person.
	2 nd	Hybrid inheritance	2 nd	Writetheclassdefinitions,theconstructors,setmethods,getmethodsandforallclasses.
	3 rd	Polymorphism: Method & constructor overloading,		
10 th	1 st	Method overriding	1 st	Writetheclassdefinitions,theconstructors,setmethods,getmethodsandforallclasses.
	2 nd	up-casting, down-casting		
	3 rd	Revision /Test	2 nd	
11 th	1 st	Abstract class& Interface	1 st	Write the classdefinitions,theconstructors,setmethods,getmethodsandforallclasses.
	2 nd	Abstract class &Interface		
	3 rd	Implementation of multiple inheritance through	2 nd	Write the class definitions, the constructors ,set

		interface		Methods, get methods and for all classes.
12 th	1 st	Implementation of multiple inheritance through interface	1 st	Write the class definitions ,the constructors ,set methods, get Methods and for all classes.
	2 nd	Implementation of multiple inheritance through interface	2 nd	9.OldMacDonalddhadafarman dseveral typesofanimals.Every animalsharedcertaincharacte ristics:theyhadatype(suchasc ow,chickorpig)andeachmade asound(moo,cluck).AnInterfa cedefinesthose thingsrequire dtobe an animal on the farm.De finenewclassesfortheOldMac Donald that implement the Animal and Farm class. Create array of object of animal to define the different types of animal in the farm.Also create a ppropriate methodsto get and s et the properties
	3 rd	Revision of Abstract class & Interface and discuss problems		
13 th	1 st	Exception Handling:	1 st	10. Write a program with Stude nta s abstract class and create d erive classes Engineering, Med icine and Science from base cla s s Student. Create the object so f the derived classes and proce s s them and access them using array of pointer of type base cla s s Student.
	2 nd	Implementation of keywords like try and catch		
	3 rd	Implementation of keywords like finally, throw & throws.		Practice of practical..
14 th	1 st	Importance of exception handling in practical implementation of live projects	1 st	Revision of practical.
	2 nd	Importance of exception handling in practical implementation of live projects		
	3 rd	Revision and problems	2 nd	Revision of practical.
15 th	1 st	Revision and problems	1 st	Revision of practical.
	2 nd	Revision and problems		
	3 rd	Revision /Test	2 nd	Revision of practical.

Lesson Plan

Name of the Faculty : Rekha Jangir

Discipline : Computer Engg.

Semester : 4th

Subject : Data Structure using C

Lesson plan duration : 15 weeks (from March 2022 to June 2022) Theory-3hr, Practical-6hrs

Week	The		Practical	
	Lecture Day	Topic (including assignments /tests)	Practical Day	Topic
1 st Week	1 st	Problem solving concept , Top-down and bottom-up design, structured programming	1 st	Exercise of C Program
			2 nd	Exercise of C Program
	2 nd	Concept of data type, variables and constants	1 st	Exercise of C Program /Revision/Practice Session
Week 2	3 rd	Introduction to data Structure(Linear, Non Linear, Primitive, Non Primitive))	2 nd	Exercise of C Program /Revision/Practice Session
	1 st	Concept of Data Structure (Array, Linked List, Stack, Queue, Trees, Graphs)	1 st	Exercise of C Program
			2 nd	Exercise of C Program
2 nd	Concept of Arrays	1 st	Program regarding Array/Revision/Practice Session	
Week 3	3 rd	One dimensional Array, Two Dimensional Array: Representation of Two dimensional Array (Base address, LB, UB)	2 nd	Program regarding Array/Revision/Practice Session
	1 st	Operational on Arrays with Algorithms (inserting, deleting)	1 st	Program regarding Array
	2 nd	Operational on Arrays with Algorithms (Searching, Traversing	2 nd	Program regarding Array
Week 4	3 rd	Introduction to linked list and double linked list, Representation of Linked list in Memory	1 st	Program regarding Array/Revision/Practice Session
	1 st	Describe and Comparison between Linked list and Array	1 st	Program of Matrices
			2 nd	Program of Matrices
2 nd	Traversing and Searching Linked List	2 nd	Program of Matrices	
Week 5	3 rd	Insertion and deletion into Linked list	1 st	Program of Matrices/ Revision/ Practice Session
	1 st	Application of Linked List and Explain Doubly Linked List	1 st	Program of Matrices/ Revision/ Practice Session
			2 nd	Program of Matrices/Revision/Practice Session
2 nd	Traversing, Insertion and deletion into doubly Linked List	2 nd	Program of Matrices/Revision/Practice Session	
Week	1 st	Implementation of Stacks	1 st	Program of addition of two Matrices using function
			2 nd	Program of addition of two Matrices using function/Revision/PracticeSession
Week	1 st	Implementation of Stacks	1 st	Program of addition of two Matrices using function/Revision/PracticeSession
			2 nd	Program of addition of two Matrices using function/Revision/PracticeSession
Week	1 st	Implementation of Stacks	1 st	Program of inserting and deleting

		Converting Infix to Post Fix Notation)	2 nd	Program of addition of two Matrices using function/Revision/PracticeSession
	3 rd	Evaluation of Post fix Notation and Tower of Hanoi	1 st	Program of inserting and deleting elements in array /Revision/Practice Session
			2 nd	Program of inserting and deleting elements in array /Revision/Practice Session
Week 7	1 st	Recursion : Concept and Comparison between recursion and Iteration	1 st	Program of Push and POP Operation in stack
	2 nd	Introduction of Queues and Implementation of queues (array and Linked list with algorithm)	2 nd	Program of Push and POP Operation in stack
	3 rd	Introduction of Queues and Implementation of queues (array and Linked list with algorithm)	1 st	Program of Push and POP Operation in stack /Revision/Practice Session
			2 nd	Program of Push and POP Operation in stack /Revision/Practice Session
Week 8	1 st	Explain Circular Queues and De-Queues	1 st	Program of Conversion from infix notation
	2 nd	Introduction of Trees and Concept of Binary Trees	2 nd	Program of Conversion from infix notation
	3 rd	Explain Complete and Extended Binary Tree	1 st	Program of Conversion from infix notation/Revision/PracticeSession
			2 nd	Program of Conversion from infix notation/Revision/Practice Session
Week 9	1 st	Concept of representation of Binary Tree	1 st	Program of the Factorial of given number using recursion
	2 nd	Concept of representation of balanced Binary Tree	2 nd	Program of the Factorial of given number using recursion
	3 rd	Explain Traversing Binary Trees (Pre Order, Post Order and In Order)	1 st	Program of the Factorial of given number using recursion /Revision/Practice Session
			2 nd	Program of the Factorial of given number using recursion /Revision/Practice Session
Week 10	1 st	Explain Searching, inserting and deleting in binary seary trees	1 st	Insertion and Deletion of elements in Queue and Circular Queue using Pointer
	2 nd	Explain Searching, inserting and deleting in binary seary trees	2 nd	Insertion and Deletion of elements in Queue and Circular Queue using Pointer
	3 rd	Problems Solution	1 st	Insertion and Deletion of elements in Queue and Circular Queue using Pointer /Revision/Practice Session
			2 nd	Insertion and Deletion of elements in Queue and Circular Queue using Pointer /Revision/Practice Session
	1 st	Test	1 st	Insertion and Deletion of elements

Week 11	2 nd	Problems Solution		in Linked List and doubly Linked list
			2 nd	Insertion and Deletion of elements in Linked List and doubly Linked list
	3 rd	Previous topic Explain	1 st	Insertion and Deletion of elements in Linked List and doubly Linked list/Revision/Practice Session
			2 nd	Insertion and Deletion of elements in Linked List and doubly Linked list/Revision/Practice Session
Week 12	1 st	Introduction of Sorting and Searching	1 st	Program of Linear Search procedures to search an element in given list
	2 nd	Search algorithm(Linear and Binary)	2 nd	Program of Linear Search procedures to search an element in given list
	3 rd	Search algorithm(Linear and Binary)	1 st	Program of Binary Search procedures to search an element in given list/Revision/Practice Session
			2 nd	Program of Binary Search procedures to search an element in given list/Revision/Practice Session
Week 13	1 st	Concept and uses of Sorting	1 st	Previous Problems solution
	2 nd	Sorting Algorithm (Bubble sort)	2 nd	Previous Problems solution
	3 rd	Sorting Algorithm (Insertion sort)	1 st	Previous Problems solution /Revision/Practice Session
			2 nd	Previous Problems solution /Revision/Practice Session
Week 14	1 st	Sorting Algorithm (Selection sort)	1 st	Program of Bubble Sort
	2 nd	Sorting Algorithm (Merge Sort)	2 nd	Program of Bubble Sort
	3 rd	Sorting Algorithm (Radix sort) & Sorting Algorithm (Heap Sort)	1 st	Program of Bubble Sort/Revision/Practice Session
2 nd			Program of Bubble Sort/Revision/Practice Session	
Week 15	1 st	Problems Solution	1 st	Program of Selection Sort
	2 nd	Problems solution	2 nd	Program of Selection Sort
	3 rd	Test	1 st	Program of Selection Sort /Revision/Practice Session
			2 nd	Program of Selection Sort /Revision/Practice Session

LESSON PLAN

Name of the Faculty:- Reenu
 Subject Computer organization
 Semester 4th
 Session March to June 2022

WEEK NO.	THEORY DAY	TOPICS COVERED	PRACTICAL
1	1	Introduction of computer hardware	N/A
	2	CPU organization	N/A
	3	Three address,two address,one address zero	N/A
2	1	RISC Instruction	N/A
	2	Addressing modes: Immediate,register,direct	N/A
	3	CPU Design: Microprog.vs hard wired	N/A
3	1	Reduced instruction set computer	N/A
	2	CISC characteristics	N/A
	3	Revision & class test	N/A
4	1	Introduction to memory	N/A
	2	Memory Hirerachy	N/A
	3	RAM and ROM chips	N/A
5	1	Memory connection to CPU	N/A
	2	Auxillary Memory	N/A
	3	Cache memory	N/A
6	1	Virtual memory	N/A
	2	Memory Management hardware	N/A
	3	Revision calss test	N/A
7	1	Introduction to O/I organization	N/A
	2	Functions of BIOS and test	N/A
	3	Test and Initialization, configuring the system	N/A
8	1	Introduction to modes of data transfer	N/A
	2	Explain Programmed I/O	N/A
	3	Assignment questions revision	N/A
9	1	Various types of interrupts	N/A
	2	DMA data transfer	N/A
	3	Revision & class test	N/A
10	1	Introduction to Architecture of Computer	N/A
	2	Multi processor systems	N/A
	3	Forms of parallel processing	N/A
11	1	introduction to Multiprocessor	N/A
	2	Multi processor systems in detail	N/A
	3	revision -class test	N/A
12	1	Forms of parallel processing	N/A
	2	Parallel processing and pipelines,	N/A
	3	Basic charactersteristics	N/A
13	1	Interconnection network	N/A
	2	Time shared bus	N/A
	3	System bus	N/A
14	1	Multi ports	N/A
	2	Cross bar switch	N/A
	3	Multi stage	N/A
15	1	Switching networks	N/A
	2	Hyper cube structures.	N/A
	3	Revision & class test	N/A

